

## How do I use Encrypted HSPICE files of Fairchild Semiconductor

- 1) Download HSPICE **partname.inc** from website
- 2) Create your top sheet with subckt call to the modelfile downloaded (ensuring that .options card found in .inc file is used)
- 3) Ensure **SEARCH= 'modelfile.inc'** is in your top sheet
- 4) Instantiate your loads and/or forcing functions ensuring that pin mapping order is maintained for subck call inside the partname.inc

### For Example: NC7SV00 in Micropak

- 1) Download nc7sv00 fst,typ,slw files
- 2) to run fst model file then create a netlist as follows:

```
** Instantiate your calls for VCC, loads etc
VPWR0 VCCEXT GNDEXT 1.4
VDATA0 A GNDEXT PULSE 0.0 3.3 5E-9 2.0E-9 2.0E-9 47.5E-9 100E-9
VDATA1 B GNDEXT DC 3.3
VGND0 GNDEXT 0 DC 0
*
*****
* LOAD
*****
R0 Y GNDEXT 2000
C0 Y GNDEXT 30p
*****
* OPTIONS
*****
*
.temp = -40
.OPTION INGOLD=2 ARTIST=2 PSF=2 post=2 search = 'nc7sv00f16'
+ PROBE=0
+ GMINDC = 1.00000E-12
+ GMIN = 1.00000E-12
+ method = gear LVLTIM = 2 chgtol = 1e-9
.plot V(A) V(B) V(Y)
.tran 1.0ns 200ns
*
XPKG A B Y VCCEXT GNDEXT nc7sv00f16 {this is top subckt call
for nc7sv00f16.inc}
.END
```

For each unique part check the .inc file for pin order to ensure Xpkg call is correct